

Burst/Pulse Verification Kit



Features

- ◇ Verification kit for GB/T_17626.4-2008 and IEC/EN 61000-4-4, amendment 2
- ◇ Peak Voltages up to 8kV
- ◇ Voltage ratio 500:1 or 1000:1
- ◇ Compatible with all burst/EFT generators
- ◇ High stability and repeatability
- ◇ Custom design available

Application

UCL-APA301/401 serial burst/EFT pulse verification kit is usually used to the periodic verification of the test system such as burst generator and immunity test analyzer etc., In order to ensure good functionality and accuracy. MS301/401 consists of two terminations with 50 Ohms and 1000 Ohms load impedance each. These terminations are rated for up to 8.0KV burst/EFT pulses and designed to comply with GB/T_17626.4-2008 and the new requirements of IEC/EN 61000-4-4: amendment 2: 2001. The verification kit can be used To verify almost all brands of burst/FET Generator with kinds of adaptors such as SHV, BNC, SOV, DN etc.

Specification

Specifications	UCL-APA 301-4000	UCL-APA 301-8000	UCL-APA 401-4000	UCL-APA 401-8000
Max power rating	3W rms		4W rms	
Max peak voltage	4kV	8kV	4kV	8kV
Max operating time	120 s (5kHz,300ms/15ms)		300 s (5kHz,300ms/15ms)	
Nominal attenuation	-54dB or -60dB		-60dB	
Nominal voltage ratio	500:1(equal to -54dB) 1000:1(equal to -60dB)		1000:1	
Input impedance	50Ω		1000Ω	
Output impedance	50Ω		50Ω	
Bandwidth (3dB)	400MHz		400MHz	
Connectors Input	SHV,BNC,SOV,DN		SHV,BNC,SOV,DN	
Output	BNC female		BNC female	
Temperature Range	-10°C~+40°C			
Dimensions	131.6x25.5x25.5mm			
Weight	140g			

* Dimensions Tolerance ±2%

** Custom designs available.